AMENDMENTS TO THE CLAIMS

This Listing of the claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

1. (Currently Amended) An allocating device for dynamically allocating bandwidth, comprising:

a plurality of personality modules, each of said personality modules having an independent bandwidth requirement, wherein at least two of said personality modules have different bandwidth requirements;

an allocation module connected to said personality modules by a plurality of transmission channels, wherein said allocation module assigns incremental bandwidths to said personality modules based upon the bandwidth requirements of said personality modules;

a plurality of slots that ~~adapted to~~ removably receive different personality modules, wherein each of said slots is connected to said allocation module by a separate transmission channel in said plurality of transmission channels;

a microprocessor connected to said allocation module by a first transmission line that programs ~~is adapted for programming~~ said allocation module to assign a bandwidth corresponding to the bandwidth requirement of said personality modules; ~~and~~

a multiplexer connected to said allocation module by a second transmission line; and

an interface circuit between the plurality of personality modules and the allocation module, the interface circuit controlling the direction of data flow between the multiplexer and the plurality of personality modules, wherein the interface circuit

comprises a plurality of bits hardwired to the slots, and wherein each slot has both dedicated data bits and data bits that are shared between neighboring slots;

wherein allocation of bandwidth to said personality modules is dynamic with respect to both changes in types of personality modules in said plurality of slots and changes in bandwidth requirements of each personality module at different times, and each personality module may reside in any slot and in any combination.

2. (Previously Presented) The device of claim 1, further comprising a controller connected to said microprocessor and said personality modules by a data line, wherein said controller obtains information from each personality module to determine how much bandwidth to assign to said personality module for transmitting data from said personality module.

3. (Previously Presented) The device of claim 2, wherein each of said personality modules is assigned incremental bandwidths with 27 Mb/s granularity.

4. (Cancelled)

5. (Previously Presented) The device of claim 1, wherein said multiplexer obtains a payload from said each of said plurality of personality modules and combines said payload for transmission over a single transmission channel.

6. (Previously Presented) The device of claim 5, wherein said allocation module further comprises an interface circuit, wherein said interface circuit comprises a set of input lines, a set of output lines, and a set of dedicated bits, and wherein said interface circuit controls the direction of said payload that flows between said multiplexer and said plurality of personality modules, and determines which of said output lines to transmit said payload on.

7. (Previously Presented) The device of claim 6, wherein said set of input lines comprises an 88-bit wide bus.

8. (Previously Presented) The device of claim 7, wherein said set of output lines comprises an 88-bit wide bus.

9. (Original) The device of claim 8, wherein said set of dedicated bits carries said payload to and from said plurality of personality modules.

10. (Original) The device of claim 9, wherein said payload is high quality uncompressed video.

11. (Original) The device of claim 9, wherein said payload is high quality uncompressed audio.

12. (Previously Presented) The device of claim 9, wherein said payload is a modulated IF carrier.

13. (Previously Presented) The device of claim 9, wherein said plurality of personality modules is selected from a group consisting of a transmit-only module, a receive-only module, and a transceiver module.

14. (Previously Presented) The device of claim 13, wherein a front panel of said allocating device comprises a connector for connecting an external device to said allocation module.

15. (Original) The device of claim 14, wherein said external device is a monitor for displaying video data.

16. to 34. (Cancelled)